



Docket No. 24061.77 (TSMC2003-0441)
Customer No. 42717

UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Chia-Chun Liu, et al. §
Serial No.: 10/823,872 §
Filed: April 14, 2004 §
For: System and Method for Identifying §
Semiconductor Process Steps for §
Queue-Time Control and Abnormality §
Detection §

Docket Number: 24061.77
(TSMC2003-0441)

Examiner: To Be Determined

Art Unit: 2825

Conf. No.: 6170

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL SHEET TO POWER OF ATTORNEY

In accordance with 37 CFR 1.32 (c)(3) and with reference to the accompanying Power of Attorney, please recognize the following ten patent practitioners as being of record in this application.

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J. Andrew Lowes	40,706
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T. Murray Smith	30,222
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Respectfully submitted,

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Dated: 8-16-04

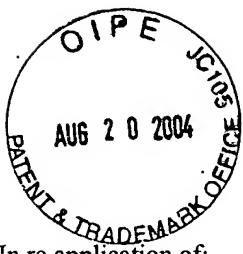
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I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date indicated below.

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Name

8-17-04
Date



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:	§	Attorney Docket No.:	24061.77
Chia-Chun Liu et al.	§		TSMC2003-0441
	§		
Serial No.: Unknown	§	Customer No. 27683	
	§		
Filed: Herewith	§	Group Art Unit: Unknown	
	§		
For: A System and Method For Identifying	§	Examiner: Unknown	
Semiconductor Process Steps For Queue-	§		
Time Control and Abnormality Detection	§		

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POWER OF ATTORNEY FOR PATENT APPLICATION

As a representative of the Assignee, Taiwan Semiconductor Manufacturing Company, Ltd., I hereby appoint the following attorneys and/or agents to prosecute this application and transact all business in the Patent and Trademark Office connected therewith.

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The undersigned is the representative for the Assignee of the entire right, title, and interest in the patent application identified above. A copy of the assignment or other documents in the chain of title are attached.

The undersigned (whose title is supplied below) is authorized to act on behalf of the Assignee.

Taiwan Semiconductor Manufacturing Company, Ltd.

Date

Chien-Wei Chou
Title: Director, IP Division